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Coding And Wavelets By Keshab K. Parhi**

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Vlsi architecture for low power variable length

without significant reduction in the image quality [15]. This paper investigates the implementation of Low Power VLSI architecture for image compression [8]

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Andrew C. Singer, A low-power VLSI architecture for turbo decoding, Design of LDPC decoders for improved low error rate performance:

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Vlsi architectures for communications and signal

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