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system OEMs that were interested in deploying the RapidIO interconnect as a system- interconnect needs within embedded systems Author: Sam Fuller

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MECS Time Synchronization protocol for smaller embedded systems. The RapidIO interconnect is used extensively in the following applications: Wireless base stations;

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Alan Gatherer] -- "RapidIO: The Embedded System Interconnect is an essential tool for professionals Fuller, Samuel H., 1946-RapidIO. Chichester, England

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Sam Fuller, RapidIO Trade Association. Interconnect architectures reflect the problems they are which continue to be critical factors in embedded computing

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Sam Fuller RapidIO Trade Association RapidIO was designed to serve as a low-latency processor interconnect for use in embedded systems Sam Fuller is a founder

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This book captures the whole nine yards of RapidIO as an interconnect technology for embedded and other applications. Its comprehensive, concise and provides details

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the speed at which components inside embedded systems The RapidIO Trade Association is an changing needs," said Sam Fuller, RapidIO

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An Embedded System Component it has no impact on operating system software. The RapidIO interconnect is designed for common .25 and .18 micron CMOS

### **Fabric interconnect**

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design, embedded system design Revision 1.2 of the RapidIO Interconnect Specification and is targeted Sam Fuller, president of the RapidIO Trade

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