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design, embedded system design Revision 1.2 of the RapidIO Interconnect Specification and is targeted Sam Fuller, president of the RapidIO Trade

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MECS Time Synchronization protocol for smaller embedded systems. The RapidIO interconnect is used extensively in the following applications: Wireless base stations;

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Sam Fuller is the author of RapidIO (0.0 avg rating, 0 ratings, 0 reviews, published 2004), Rapidio (0.0 avg rating, 0 ratings, 0 reviews,

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I've been reading the spec and also "RapidIO The Embedded System Interconnect" by Sam Fuller and had seen this since writing my original post.

Rapidio - xilinx

An Embedded System Component it has no impact on operating system software. The RapidIO interconnect is designed for common .25 and .18 micron CMOS

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